

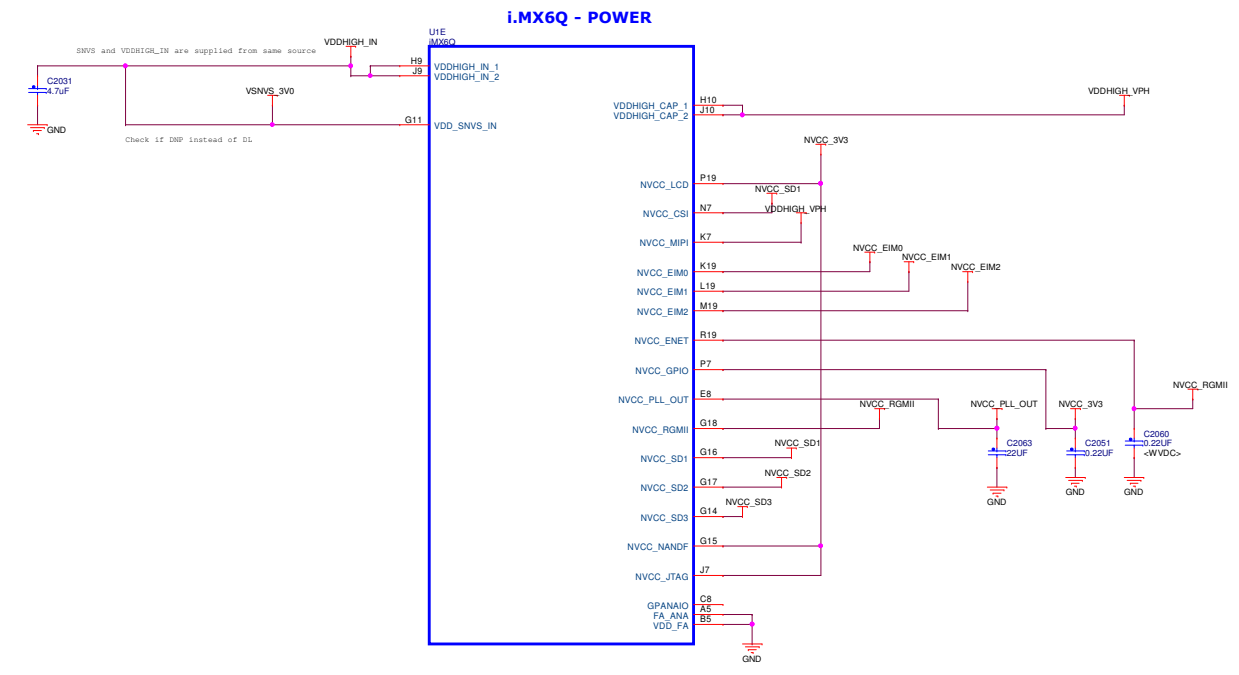
SR-uSOM-MX6

Simplified schematics for SR-uSOM-MX6
i.MX6 based Micro-SOM.

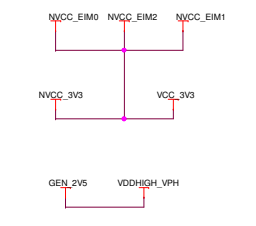
Assembly options naming convention.

When the following appears near a component
then it can be assembled in all cases or
conditionally assembled in certain cases -

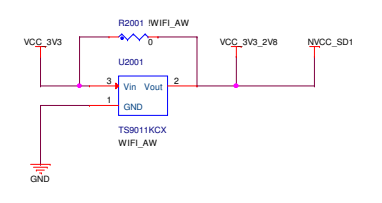
1. No name - always assemble
2. DNP - Do not place
3. FE - fast ethernet
4. GE - gigabit ethernet
5. WIFI_USI- BRCM4329 based WIFI and BT option
6. WIFI_AW - BRCM4330 based WIFI and BT option
7. !WIFI_AW - Not using BRCM4330 based WiFi and BT



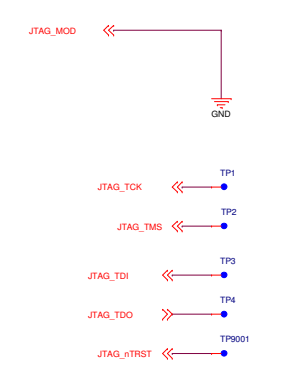
Solo and DL can only support stable EMI1/2/3
In this design NVCC_EIM0,1,2 are shorted



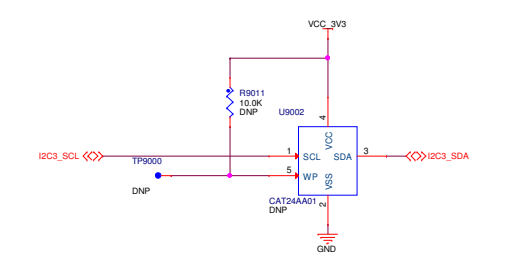
BCM4330 max SDIO I/F voltage level is 3.0
This LDO is required in this case and lowers
the voltage levels to 2.8V.
Notice that the main GATE for debug is also
on this power rail, due to that when using a
BCM4330 based WiFi/BT module then
UART1 voltage levels would be 2.8V

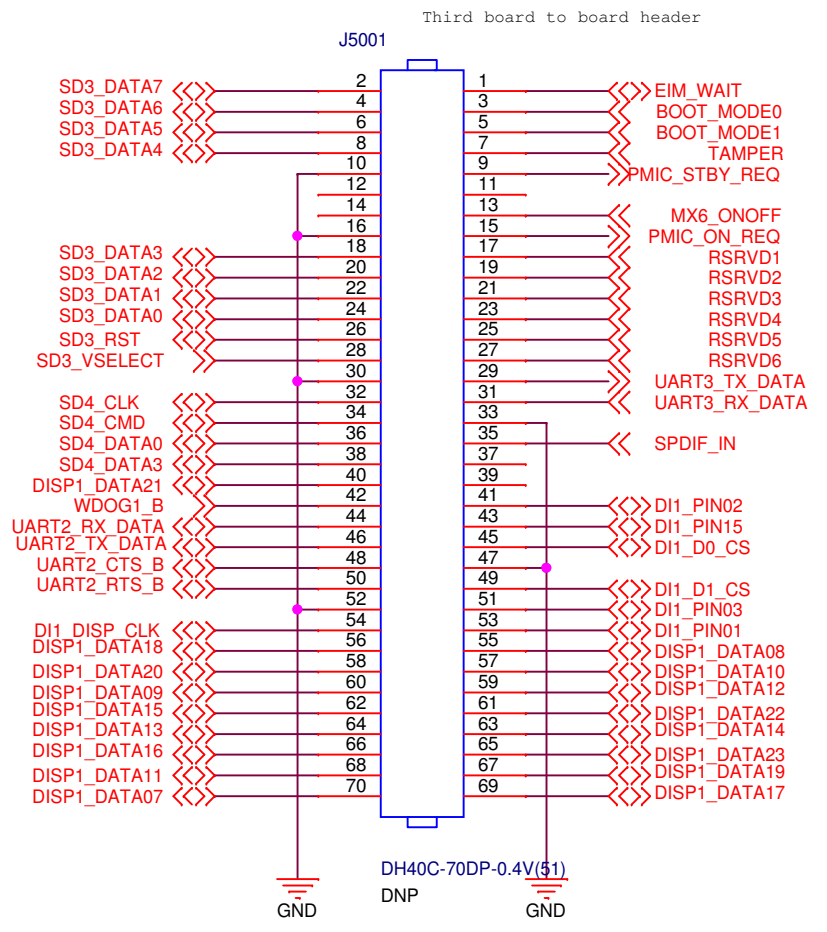
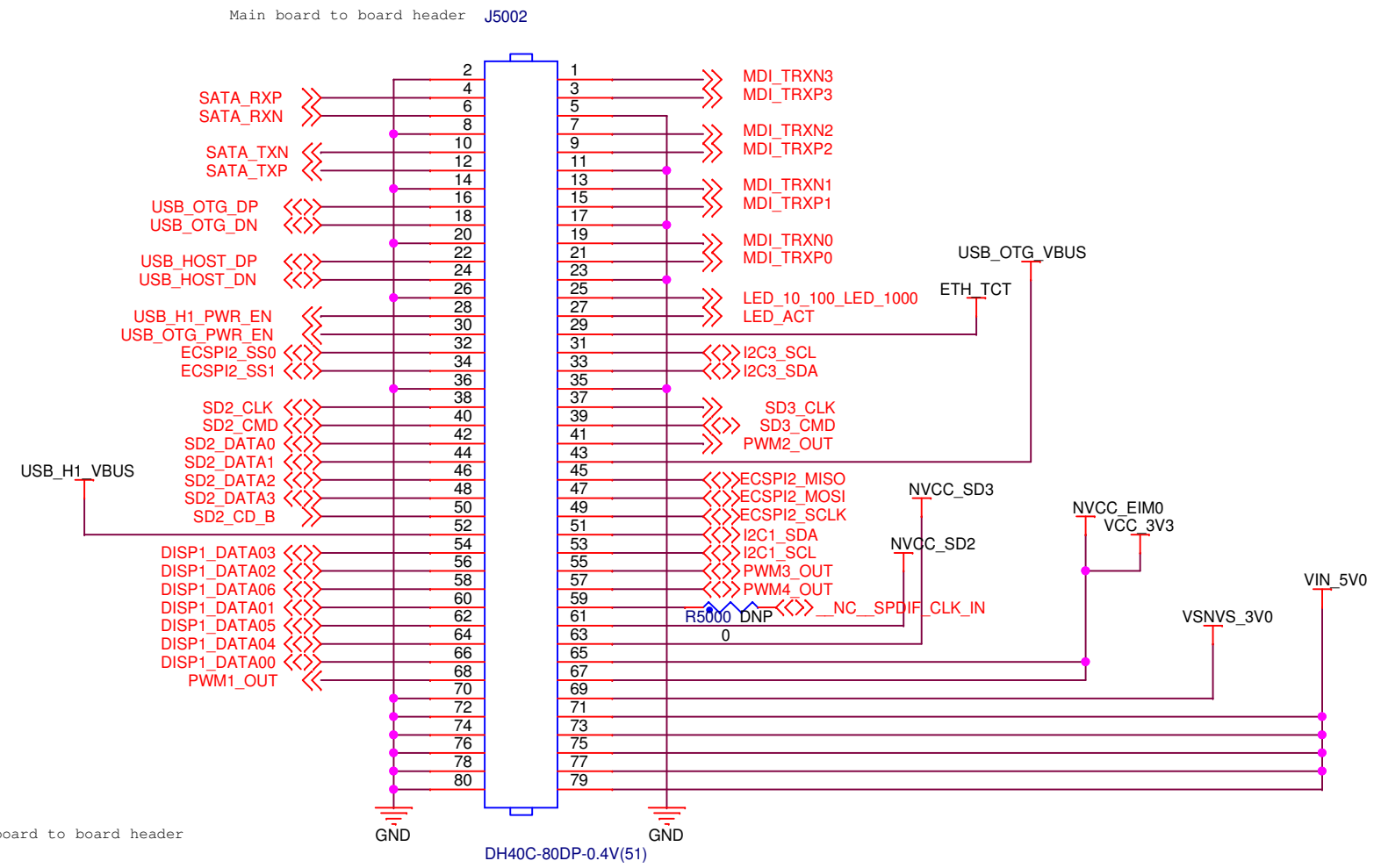
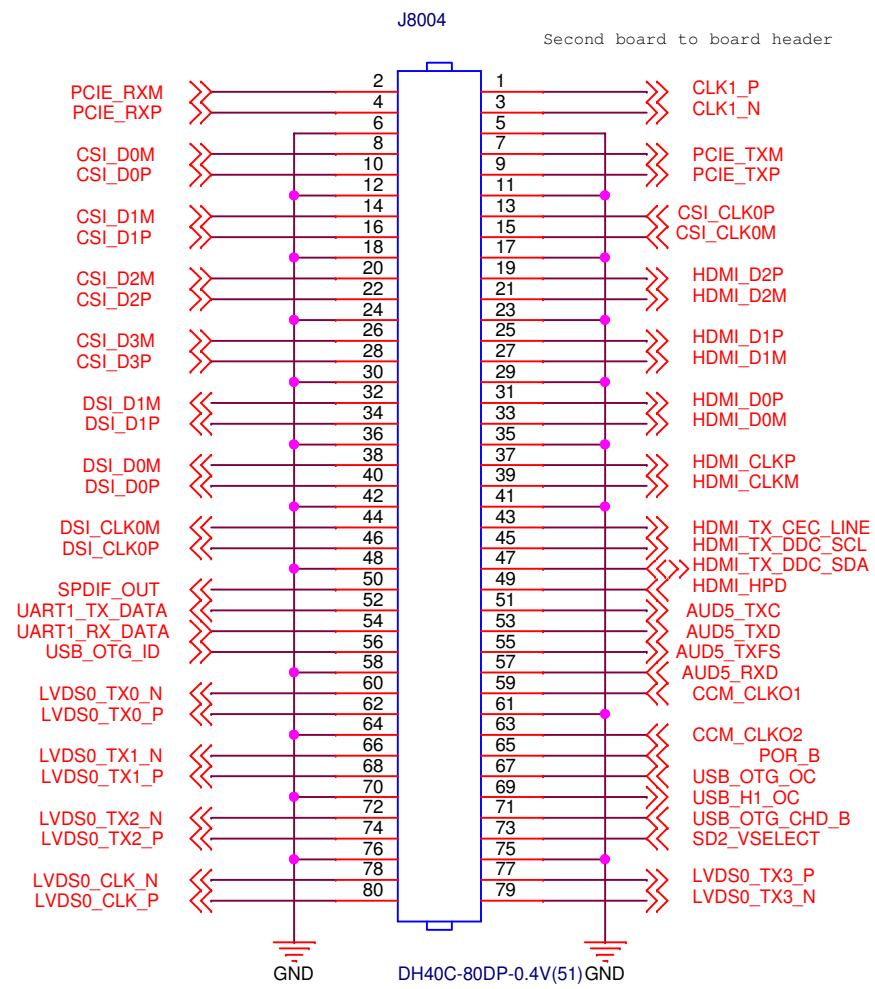


JTAG interface -
Refer to the iM6Q-DS000 reference manual
with regards placement of the JTAG test points.



Serial flash,
Typically unassembled.



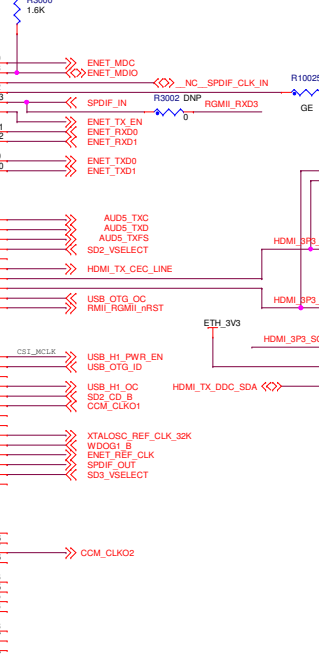
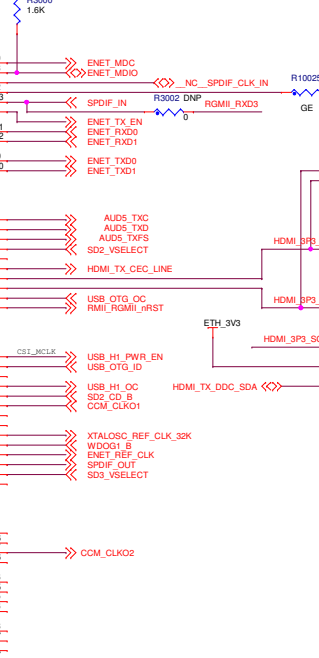
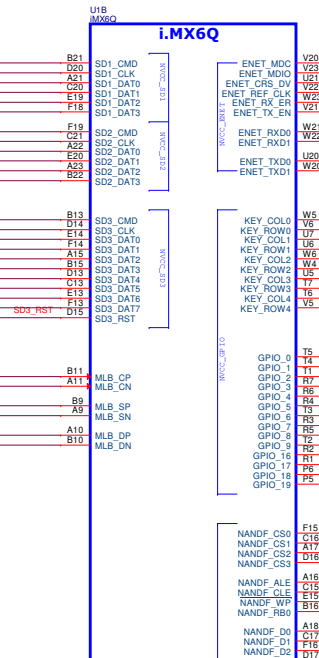
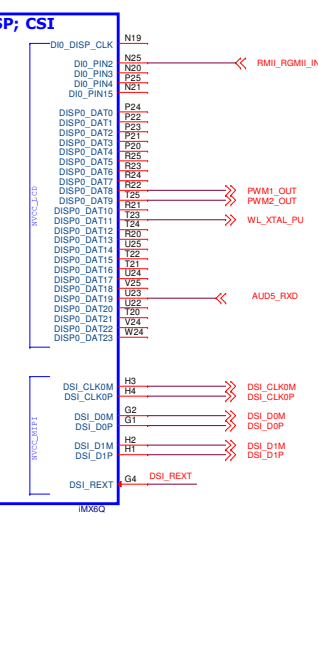
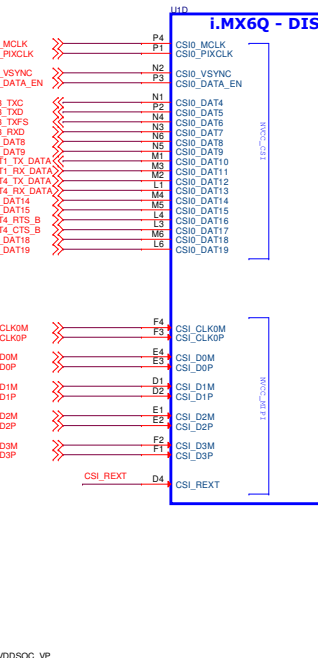
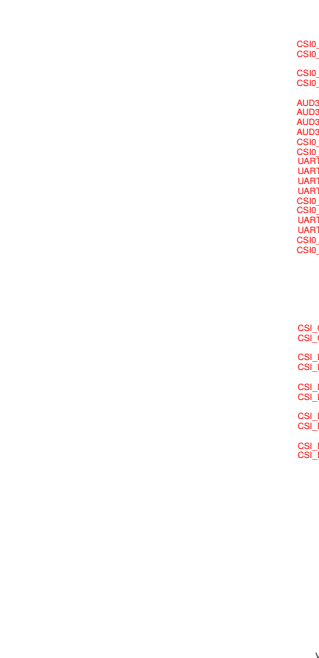
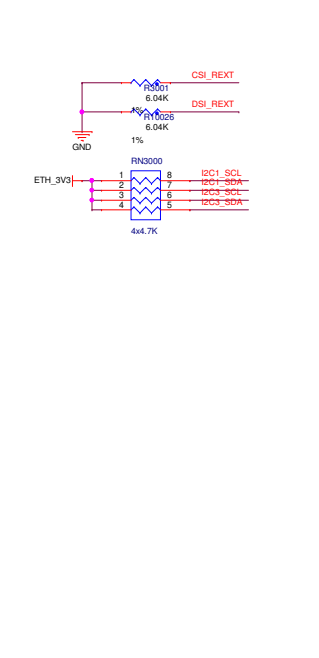
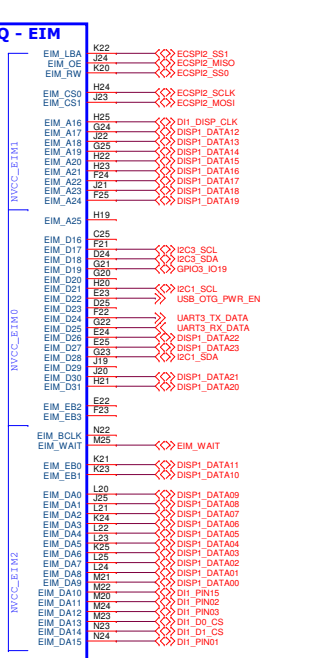


Third board to board header note-
By default CuBox-i and HummingBoard do not use this header. Due to that typical build of CuBox-i and HummingBoard comes without this header assembled.
In generat the SR-uSOM-MX6 requested from SolidRun can have this header assembled.

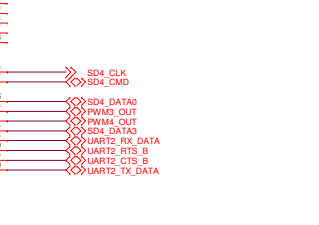
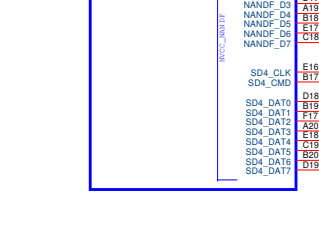
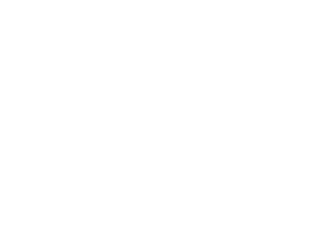
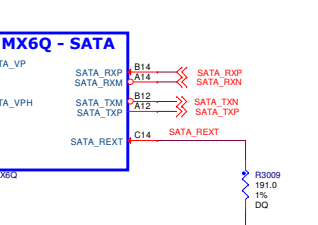
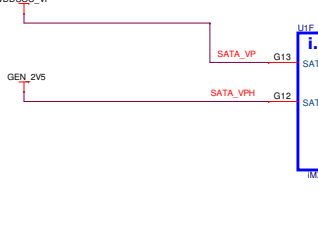
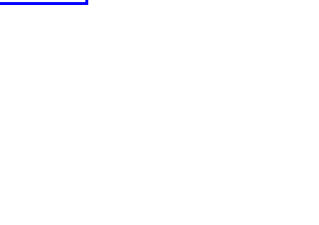
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Size B	Title SR-uSOM-MX6 - Micro-SOM to carrier board connectivity	Rev 1.3
Date: Monday, June 23, 2014	Sheet 3 of 6	

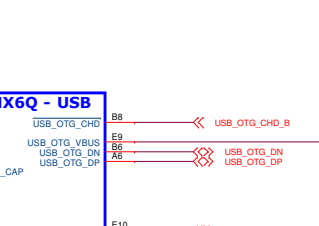
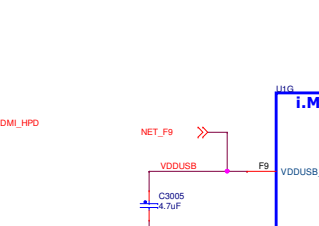
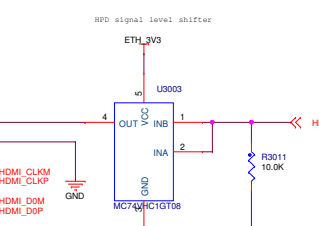
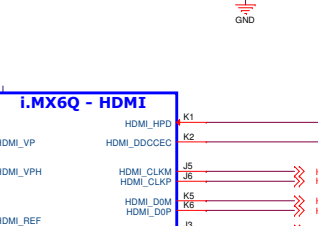
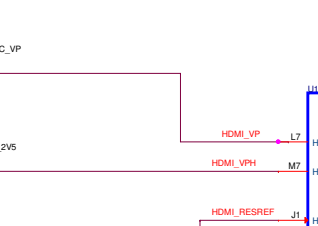
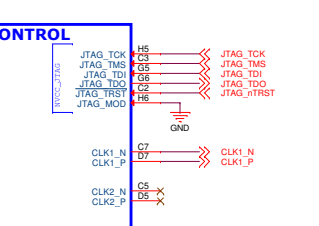
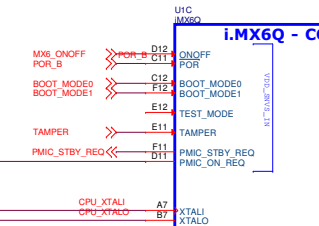
U1A
M06Q



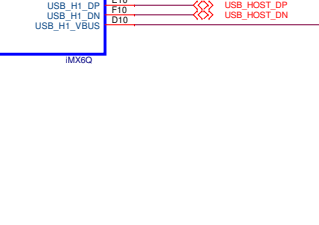
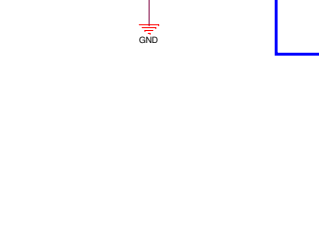
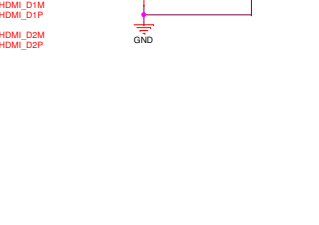
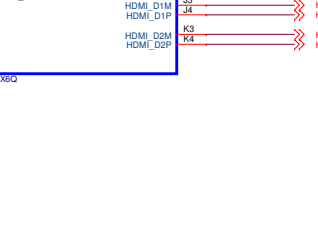
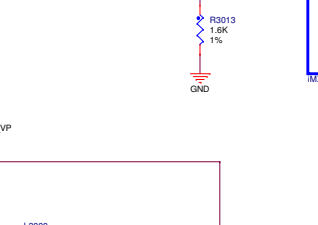
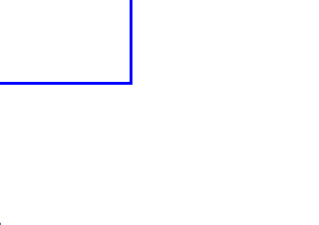
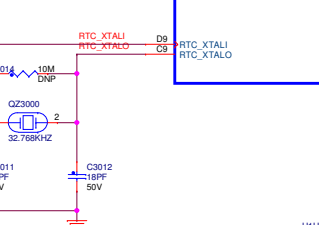
U1B
M06Q



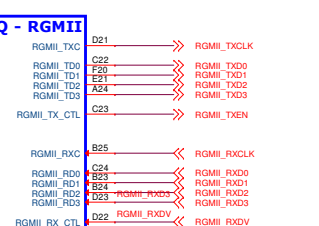
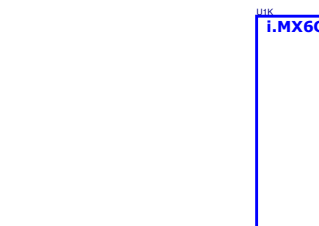
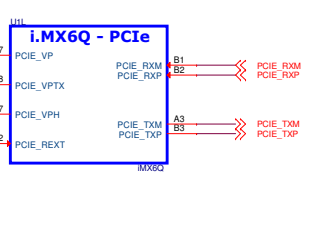
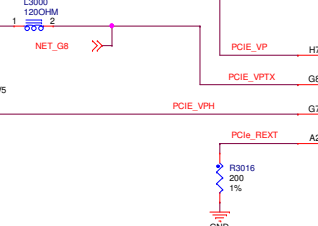
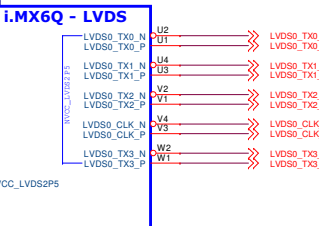
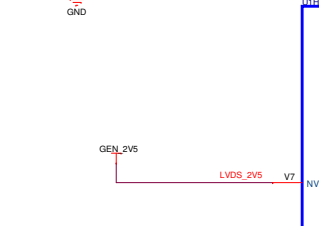
U1C
M06Q



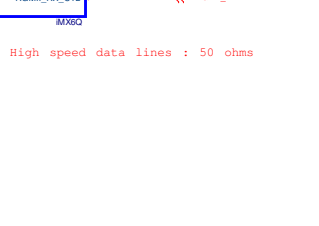
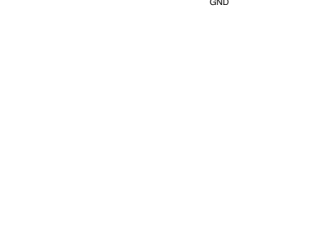
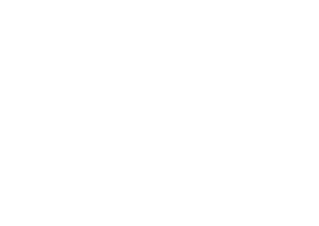
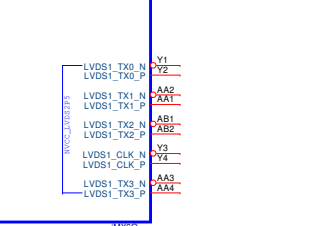
U1D
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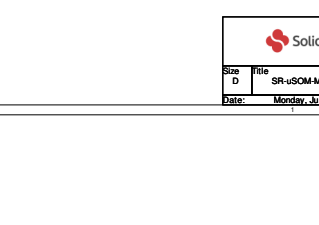
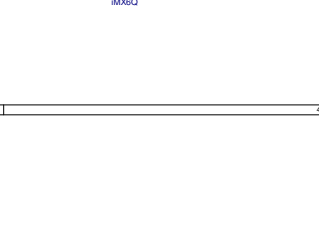
U1E
M06Q



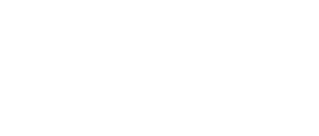
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M06Q



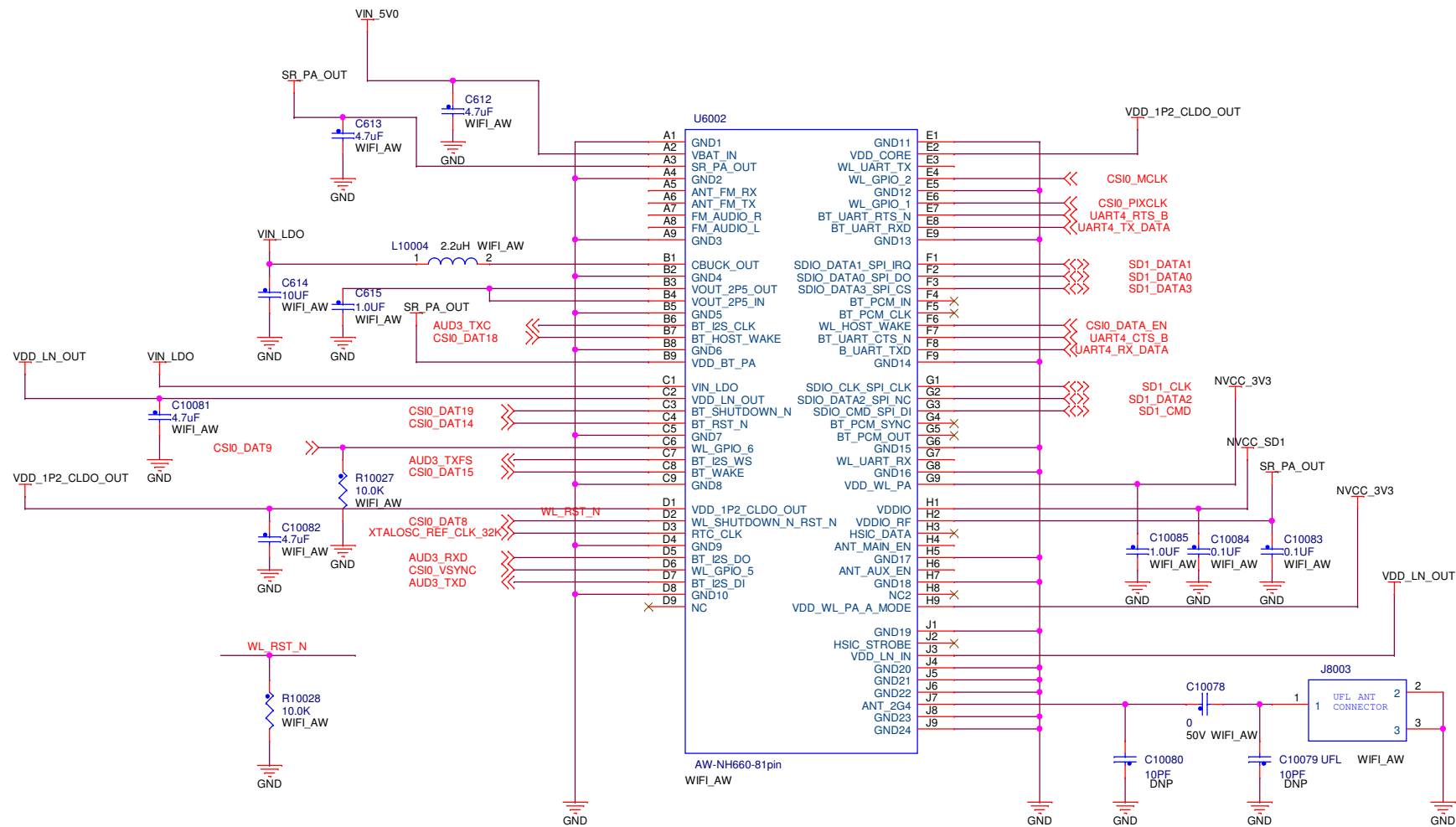
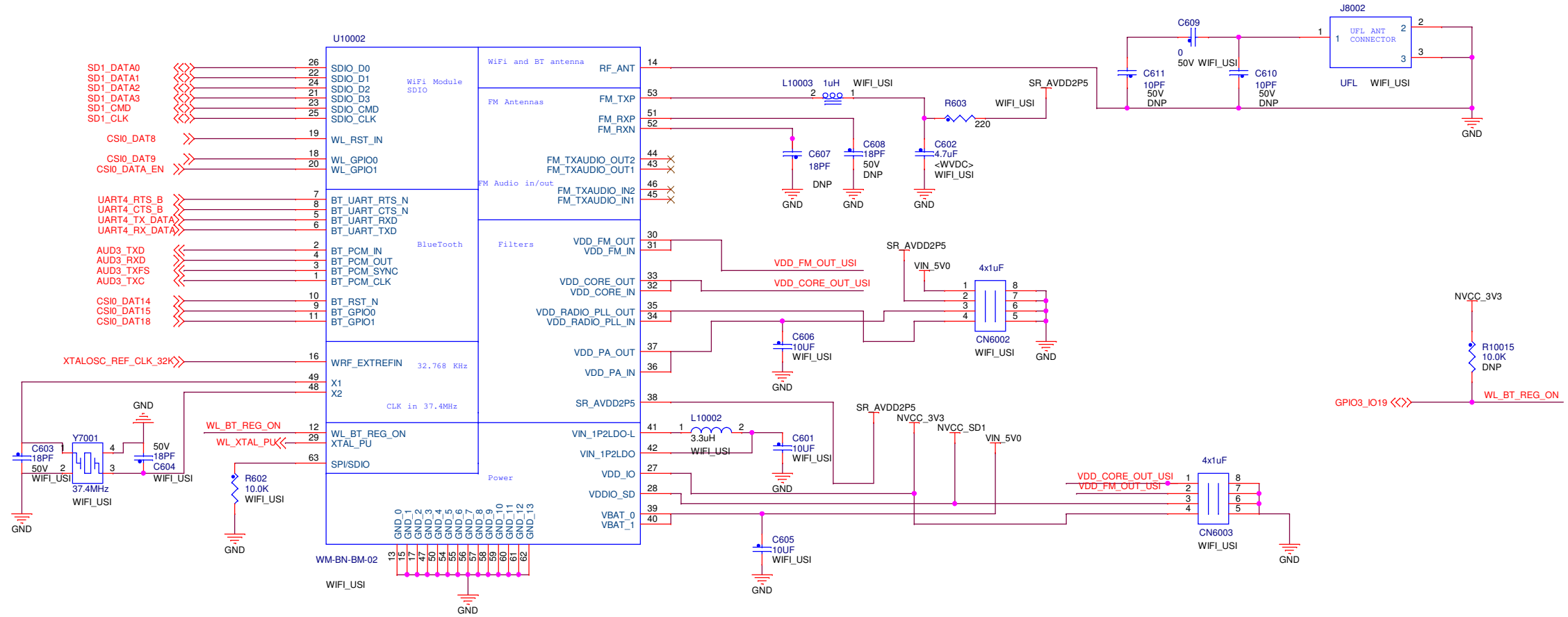
U1G
M06Q



U1H
M06Q



Layout: High speed data lines : 50 ohms



- Notes -
1. Only one of the BCM4329 and BCM4330 modules can be assembled at a time.
 2. Both those modules shared the SDIO, UART and Audio interfaces connected to the i.MX6 SoC.
 3. The UFL antenna connectors are placed one on top of the other on the layout. This means that only one can be assembled and not both.
 4. Although C609 and C10078 are described as a capacitor but are assembled as zero ohm resistor.

VCC_3V3 ETH_3V3

